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PTO Form-1449 (only)



Inventor(s): Shuichi Takayama et al.

Title: **A PROCESSOR FOR EXECUTING INSTRUCTIONS IN UNITS THAT ARE UNRELATED TO THE UNITS IN WHICH INSTRUCTIONS ARE READ, AND A COMPILER, AN OPTIMIZATION APPARATUS, AN ASSEMBLER, A LINKER, A DEBUGGER AND A DISASSEMBLER FOR SUCH PROCESS**

Attorney's
Docket No.: NAK1-BG86b

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INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)

NAK1-B

Application Number

#2

Applicant(s)

Takayama et al.

Filing Date

Group Art Unit

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		5,559,975	9/1996	Christie et al.			
		5,218,711	6/1993	Yoshida			
		5,819,081	10/1998	Nanba			
		5,475,822	12/1995	Sibigroth et al.			
		6,088,779	7/2000	Bharadhwaj			

3C903 U.S. PRO
10/051335
10/19/01

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
		6242237	2/1987	Japan				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.